

**PROPOSTA DI ISTITUZIONE DEL CORSO DI DOTTORATO IN  
INGEGNERIA ELETTRONICA, INFORMATICA ED ELETTRICA**

**CICLO 33**

**Produzione scientifica dei membri del collegio dal 2012 al 2016: vengono riportate per ciascuno le (max) 5  
pubblicazioni ritenute più significative ricomprese nelle tipologie VQR**

**Prof. Slot Krzysztof**

Polańczyk, M., Strzelecki, M., Ślot, K.

Obstacle avoidance procedure and lee algorithm based path replanner for autonomous mobile platforms

(2013) International Journal of Electronics and Telecommunications, 59 (1), pp. 85-91

DOI: 10.2478/eletel-2013-0010 - DOCUMENT TYPE: Article

Ślot, K., Kornatowski, Ł., Dębiec, P.

Fast texture synthesis with cellular neural network-based patch stitching

(2012) International Journal of Circuit Theory and Applications, 40 (12), pp. 1265-1275.

DOI: 10.1002/cta.1798 - DOCUMENT TYPE: Article

Ślot K., Owczarek A., Janczyk M.:

Vision based human-machine interfaces: visem recognition.

(2014) Computer vision in robotics and industrial applications. pp. 173-194, World Scientific, Singapore.

DOI: 10.1142/9789814583725\_0009 - DOCUMENT TYPE: Book Chapter

Ślot K., Strzecha K., Nowak G.

Image analysis-based estimation of metallic and pearl add-ons concentrations in automotive paints.

(2015) Image Processing & Communications, 19 (2-3), pp. 119-126.

DOI: 10.1515/ipc-2015-0016 - DOCUMENT TYPE: Article

Ślot K., Adamiak K., Duch P., Żurek D.

Supervised kernel principal component analysis by most expressive feature reordering.

(2015) Journal of Telecommunications and Information Technology, 2, pp. 3-10.

ISSN: 1509-4553 - DOCUMENT TYPE: Article