Francesco Svelto

Curriculum Vitae, September 2016

Prof. Svelto received the Laurea and Ph.D. degrees in electrical engineering from University of Pavia, Pavia, Italy, in 1991 and 1995, respectively.

During his Ph. D. studies, focused on the design of low noise electronic circuits for high energy physics experiments, he was Associate Researcher for a few months in several international institutions: Brookhaven National Laboratory (U.S.), Lawrence Berkeley National Laboratories (U.S.), C.E.R.N. (Switzerland), Laboratoire du l' Accelerateur Lineaire (France). From 1996 to 1997, he held a grant from STMicroelectronics for research in the area of CMOS wireless circuits. In 1997, he was appointed Assistant Professor at University of Bergamo, and in 2000 he joined University of Pavia where he is now Professor and Vice-Rector for knowledge transfer.

He has been technical advisor of RFDomus Inc., a start-up he co-founded in 2002 dedicated to highly integrated GPS receivers. After merging with Glonav Inc. (Ireland), RFDomus has been acquired by NXP Semiconductors in 2007.

Since January 2006 he has been the Director of a Scientific Laboratory, joint between University of Pavia and STMicrolectronics, dedicated to research in Microelectronics. Research interests are in the field of RF and mm-wave systems for wireless communications, high-speed serial links and ultrasound electronics for medical diagnostic. Silicon photonics, both in the area of optical components and electronics transceiver design, is drawing more and more research interest. The Laboratory has issued more than 100 grants, filed 27 patents.

Dr Svelto has been involved in 6 European Projects, 1 each within the 4^{th}, 5^{th}, 6^{th} framework and in Horizon and 2 in the 7^{th} framework. He has been involved in 2 Italian Projects FIRB and in 2 PRIN as principal investigator. Prof. Svelto has published more than 135 publications in ISI web of Science, that have been collectively cited more 2000 times, and the h-index is 25. These are considered very good achievements in the field of Electronic Engineering, specifically in the hardware sub-discipline.

He has been a member of the technical program committee of the International Solid State Circuits Conference, the Custom Integrated Circuits Conference, and the Bipolar/BiCMOS Circuits Technology Meeting: all organized by the Institute of Electrical and Electronics Engineers (IEEE).

He is presently a member of the technical program committee of the European Solid State Circuits Conference. He served as Associate Editor of the IEEE Journal of Solid-State Circuits (2003–2007), and as Guest Editor for a special issue on the same journal in March 2003.

He is co-recipient of the IEEE Journal of Solid State Circuits 2003 Best Paper Award.

Presently, he is Fellow of the IEEE and Solid State Circuits Society Distinguished lecturer.